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[Xilinx ISE Design Suite 14.5 Crack](#)

The screenshot shows the Xilinx ISE Design Suite 14.5 interface. On the left, the 'Source Libraries' window shows a project named 'work' containing files: 'Contador.vhd', 'CustomDataTypes.vhd', 'GenericDEMUX.vhd', and 'MequinaDeEstados.vhd'. The main editor displays a VHDL file named 'CustomDataTypes.vhd' with the following code:

```
1  --  
2  -- Package File Template  
3  --  
4  -- Purpose: This package defines supplemental types, subtypes,  
5  -- constants, and functions  
6  --  
7  -- To use any of the example code shown below, uncomment the lines and modify a  
8  --  
9  --  
10 library IEEE;  
11 use IEEE.STD_LOGIC_1164.all;  
12  
13 package CustomDataTypes is  
14   type Matrix is array (natural range<>) of STD_LOGIC_VECTOR (natural range<>);  
15  
16   -- type <new_type> is  
17   -- record  
18   --   <type_name>          : std_logic_vector( 7 downto 0);  
19   --   <type_name>          : std_logic;  
20   -- end record;  
21  
22   -- Declare constants  
23  
24   -- constant <constant_name>    : time := <time_unit> ns;  
25   -- constant <constant_name>    : integer := <value>;  
26  
27   -- Declare functions and procedure  
28 --
```

The bottom status bar shows two errors:

- Line 14. parse error, unexpected NOTCONSTRAINT
- Line 35. Package CustomDataTypes does not exist.

A warning is also visible: 'WARNING:ProjectMgmt - File F:/Datos/Micro UTM/FGPA/M'

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